Upcoming Conferences: calls and more - Jan 26th

Brian Bailey - January 26, 2012

If you have an upcoming conference related to EDA tools, techniques, IP or design practices and would like me to include your call for papers, contribution or participation in this conference roundup, then please send me an email.

In this issue:

Call for Papers

IEEE/ACM International Conference on Computer-Aided Design (ICCAD)

19th Annual Electronic Design Process Symposium

Indian SystemC Users Group Meeting (ISCUG)

International Conference on Embedded Computer Systems: Architectures, Modeling, and Simulation (SAMOS XII)

5th Workshop on Mapping of Applications to MPSoCs

15th International Workshop on Software and Compilers for Embedded Systems

Conference Participation

DesignCon 2012

This coming week is DesignCon down in Silicon Valley and I hope you will be there. The conference runs from January 30th through February 2nd and the exhibition is from the 31st to the 1st. All of the details are available on their website http://www.designcon.com

I will be moderating a panel on Tuesday 31st at 3:45 titled “Is it Time for an Analog Comeback?” and will be joined by panelists from EDA, IP and Systems companies. I hope to see many of you there.

Call for Papers

IEEE/ACM INTERNATIONAL CONFERENCE ON COMPUTER-AIDED DESIGN

California, 2012 www.iccad.com
DEADLINE FOR ELECTRONIC SUBMISSION OF PAPERS:

Monday, April 16, 2012 - 5:00pm Pacific Daylight Time (GMT-7)

ICCAD serves EDA and design professionals, highlighting new challenges and innovative solutions for Integrated Circuit Design Technologies and Systems. ICCAD covers the full range of CAD topics - from device and circuit-level CAD up through system-level CAD and embedded software, as well as CAD for post-CMOS design and novel application areas, such as biology and nanotechnology.

Paper submissions must be done through the online submission system at: http://www.easychair.org/conferences/?conf=iccad2012. Regular papers will be reviewed as finished papers; preliminary submissions will be at a disadvantage.

Full details available here

19th Annual Electronic Design Process Symposium

Monterey Beach Hotel, Monterey California April 5 & 6, 2012

The Electronic Design Processes (EDP) 2011 Symposium provides a forum for a cross-section of the design community to discuss state-of-the-art electronics design processes and CAD methodologies.

The Workshop focuses on the improvement of the overall design process, rather than on the functions of the individual tools themselves.

Visit http://www.eda.org/edps to see the list of past EDPS Workshop speakers and presentations.

Authors should submit full-length original and unpublished papers along with author contact information. Proposals for special, poster, and panel sessions may also be submitted; a 1-page description along with organizer contact information is required.

Send papers and Proposals to: edps@eda.org.

Important Dates:

Feb. 29 - Submission Deadline
Mar. 16 - Acceptance Notification
Mar. 30 - Camera Ready Copy
Apr. 5 - On-site Registration

Important Web Sites:

www.eda.org/edps
www.montereybeachresort.com

ISCUG 2012: CALL FOR CONTRIBUTIONS

Indian SystemC Users Group Meeting (ISCUG)
The Indian SystemC User's Group (ISCUG) aims to accelerate the adoption of SystemC in the Semiconductor Industry. ISCUG provides a platform for the SystemC beginners, SystemC experts, ESL managers and ESL vendors to share their knowledge, experiences & best practices about SystemC usage.

Abstract Deadline: Wednesday, 15th Feb 2012

Submit your abstract up to 200 words at: http://www.iscug.in/submit_abstract

The final contribution will be a presentation of about 25 minutes duration. The topic of the presentation can include all the use cases of SystemC including but not restricted to: Virtual platform for embedded software development, Architectural exploration, Performance optimization, High level synthesis (HLS), Analog Mixed-Signal (AMS) modelling, etc... The presentation should not appear like a sales presentation and should not talk about any specific tools/offering from any ESL vendor.

You may submit the abstract for the following category of sessions:

* Getting Started with SystemC
* Advanced modelling techniques
* ESL success story

International Conference on Embedded Computer Systems: Architectures, Modeling, and Simulation (SAMOS XII)

Samos, Greece, July 16-19, 2012 http://samos.et.tudelft.nl/

SUBMISSION DEADLINE: February 10, 2012

SAMOS is a premier and well-established conference on embedded systems organized annually since 2001. The conference brings together researchers from academia and industry on the quiet and inspiring northern mountainside of the Mediterranean island of Samos. It provides an environment where collaboration rather than competition is fostered.

The conference considers new state-of-the-art mature research papers on all aspects of embedded processor hardware/software design and integration. SAMOS XII has IEEE technical co-sponsorship by the IEEE CAS Society (pending) and the IEEE SSCS Germany Chapter and the conference proceedings will be published in the IEEE Xplore (pending). Authors are invited to submit technical papers in accordance to the author's instructions describing original work in one of the following tracks:

Novel Architectures and Micro-architectures for Embedded Systems

* Application-specific and Domain-specific Embedded Systems
* Embedded Reconfigurable Processors
* Multimedia and Graphics Architectures and Processors
* Energy-Aware and Low-Power Processors
The workshop on Software and Compilers for Embedded Systems (SCOPES) and the workshop on Mapping of Applications to MPSoCs (Map2MPSoC) will organize a joint Map2MPSoC/SCOPES workshop in 2012.

The Map2MPSoC/SCOPES workshop will feature a combination of research papers and research presentations (details see below). The research papers will also be published in the ACM digital library. The workshop will be held in cooperation with ACM SIGBED and EDAA.

Topics of interest include all aspects of the compilation and mapping process of embedded single and multi-processor systems. This includes (but is not limited to):

- models of computation and programming languages;
- performance analysis techniques for models of computation;
- automatic code parallelization techniques;
- mapping and scheduling techniques for for embedded multi-processor systems;
- code generation techniques for embedded single and multi-processor architectures;
- design-space exploration techniques for use in the HW/SW codesign process;
- techniques to exploit the dynamic behavior in embedded applications;
- interactions between operating systems and compilation techniques;
- techniques for compiler aided profiling, measurement, debugging and validation of embedded software.

IMPORTANT DATES

Research papers

Abstract submission research papers: Feb 17, 2012
Full research paper submission: Feb 24, 2012 (hard deadline)
Notification of acceptance: Mar 19, 2012
Final paper submission: Apr 2, 2012

Research presentations

Abstract submission research presentations: Mar 26, 2012
Notification of acceptance: Apr 2, 2012

Brian Bailey – keeping you covered

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