



[Picotest announces power integrity workshops in 6 cities](#)

[Martin Rowe](#) - July 07, 2017

Power integrity has become a significant issue with high-speed circuits. All of those signals pulling high and low, combined with the current they draw and impedance in your power delivery network (PDN) can cause signal-integrity problems.

Power integrity isn't so easy to understand. Indeed, entire books have been written about the topic, including one from [Steve Sandler](#) (see link below to a review). Now Sandler is taking to the road to deliver a 2+1 day power integrity workshop in six cities.



The two-day [Hands-On Power Integrity Workshop](#) will take place in:

- Phoenix, Arizona, September 6 and 7, 2017
- Detroit, Michigan, September 26 and 27, 2017
- Baltimore, Maryland, October 18 and 19, 2017
- Seattle, Washington, November 7 and 8, 2017

- Santa Clara, California, November 28 and 29, 2017
- Dallas, Texas, January 9 and 10, 2018



Attendees will get a combination of lecture and hand-on labs using real test equipment. Sandler has presented many papers on power integrity, impedance, and power design at conferences such as DesignCon and EDICON. He is a regular contributor to EDN on PDN design and measurement. Attendees of the 2-day workshop will receive a signed copy of Sander's book, *Power Integrity*.

Lectures will cover the importance of PDN design, simulation, and other techniques. The lab exercises will help you understand component, power supply, and PDN measurements, including the 2-port shunt-through impedance measurement, stability, power supply rejection ratio (PSRR), and ripple.

Picotest also offers a third day workshop, [Hands-On VRM Modeling and Decoupling Optimization](#), on the days immediately following the dates and locations above.

In the VRM Modeling and Decoupling Optimization Workshop, Sandler will teach how VRMs (Voltage Regulator Modules), printed circuit boards, and decoupling affect system performance. He will also cover modeling the VRM and optimizing decoupling component selection. The workshop's hands-on segment will cover measurement and simulation steps required to create measurement-based VRM and decoupling models.

Prices: Hands-On Power Integrity Workshop (2-day) \$1500; Hands-On VRM Modeling and Decoupling Optimization (1-day) \$750, or both for \$2150. Workshops are limited to 20 attendees.

—[Martin Rowe](#) covers test and measurement for [EDN](#) and [EE Times](#). Contact him at martin.rowe@aspencore.com  

Also see

- [Book Review: Power Integrity](#)
- [Impedance measurement rescues, Sandler's EDN blog](#)
- [Power Integrity: It's not just decoupling caps](#)
- [PCB characteristics affect PDN performance](#)
- [Simple trick to measure plane impedance with a VNA](#)
- [Collection: Bogatin's Rules of Thumb](#)