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DESIGN FEATURES

Video switches route analog signals along paths of least resistance

Analog video is very much alive and well despite the advance of digital video. By properly using wideband, low-distortion switches and drivers, you can guide signals to their intended destinations. Various switching configurations provide flexibility in routing as well as path con-

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Fight corruption, preserve purity with analog-signal isolation

Analog-signal isolation isn't just for preventing injury and damage from high voltages. It can dramatically reduce noise and artifacts that corrupt sensitive measurements.

—Dan Strassberg, Senior Technical Editor

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Home-automation networks mature while the PC industry chases a new home LAN

Two types of networks are headed into the home. Control, or home-automation, network technology has finally matured enough to promise widespread deployment. Meanwhile, the PC industry is furiously pursuing new data LANs for the home. IC, card, and system vendors must choose the right LAN and meld it with the right control

101

Subtleties count in wide-dynamic-range analog interfaces

Transporting high-dynamic-range analog signals from one piece of equipment to another is not a trivial task. Even subtle design variations can make huge differences in the equipment's ability to reject interference from the ac power line and other sources when the equipment connects to a real-world system.

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Sniffer probe locates sources of EMI

A miniature EMI "sniffer probe" and an oscilloscope can help to locate and identify magnetic-field sources of EMI. Small size with relatively high sensitivity and electrostatic shielding enable the probe to provide much more information than other available probes.

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Debugging embedded systems

If your μ C-based design is short on pins, you can perform diagnostics via only one pin by implementing a serial condition monitor.


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Registered-output FSMs synchronize outputs to state transitions

State machines with both registered next-state transitions and registered outputs can deliver higher performance, lower power consumption, greater silicon efficiency, easier modification, and more predictable operation than traditionally coded alternatives.

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LEADING EDGE

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<i>—Jim Lipman, Technical Editor</i>	

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