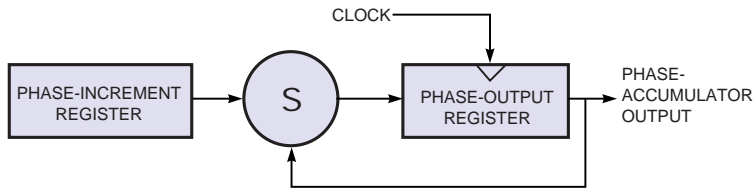


Figure 2



An SFG uses a clock generator that is based on a direct-digital-synthesis technique called "phase accumulation" (adapted from material furnished by Wavetek).