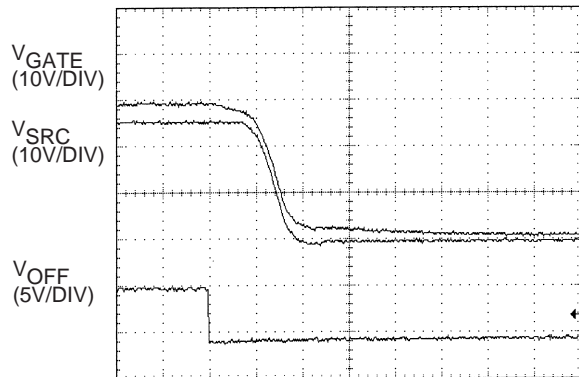
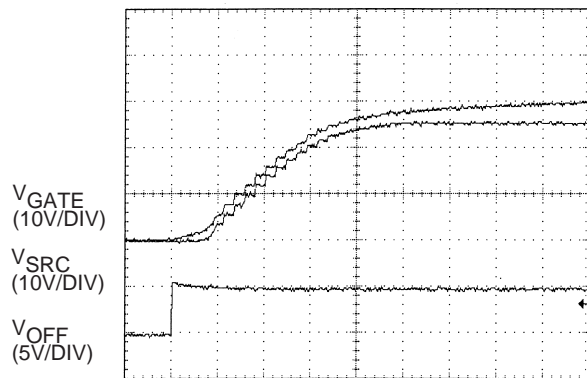


**Figure 2**



**a)**



**b)**

With Figure 1's load-current trip threshold set at 1A, the load voltage,  $V_{SRC}$  (middle waveform), turns off (a) and on (b) in approximately 7 and 400  $\mu$ sec, respectively, under the control of the signal at IC<sub>2</sub>'s  $V_{OFF}$  pin.