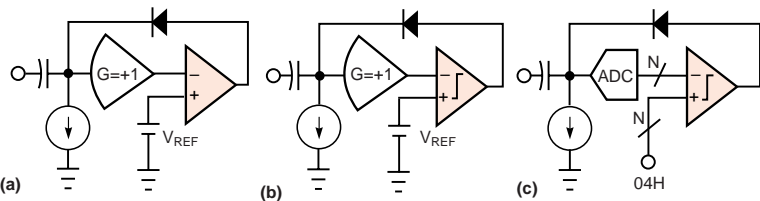


Figure 1



Sync-tip clamping using either an op amp (a) or a comparator (b) is a common technique for sync detection. Digital sync-tip clamping (c) embeds an ac-coupled ADC in the clamping feedback loop, a scheme that performs dc restoration without horizontal-timing information.