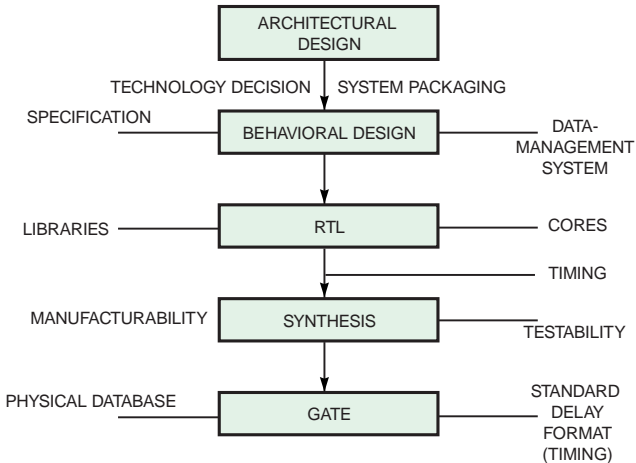


Figure 1



The design process for system-on-chip designs begins with architectural definition and leads to a physical database and on-chip delay information for timing verification.