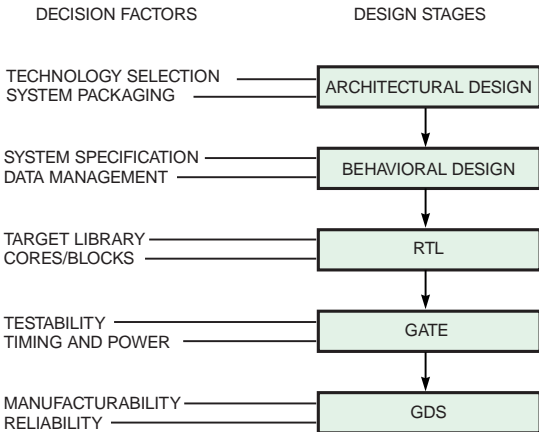


# Figure 2



**Each stage of a system-on-chip design requires you to make decisions that affect the compliance of the entire system or parts of the system to specifications.**