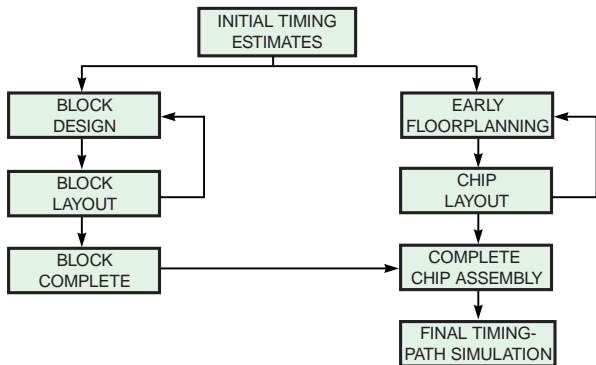


Figure 4



You need timing estimations at block and full-chip levels. A floorplanning tool and block-timing information (as it becomes available) can help with full-chip timing estimation.