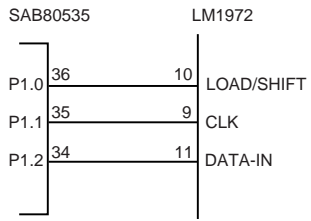


Figure 1



The rising edge of the clock signal validates data loading into the Data-In and Load/Shift pins of the potentiometer.