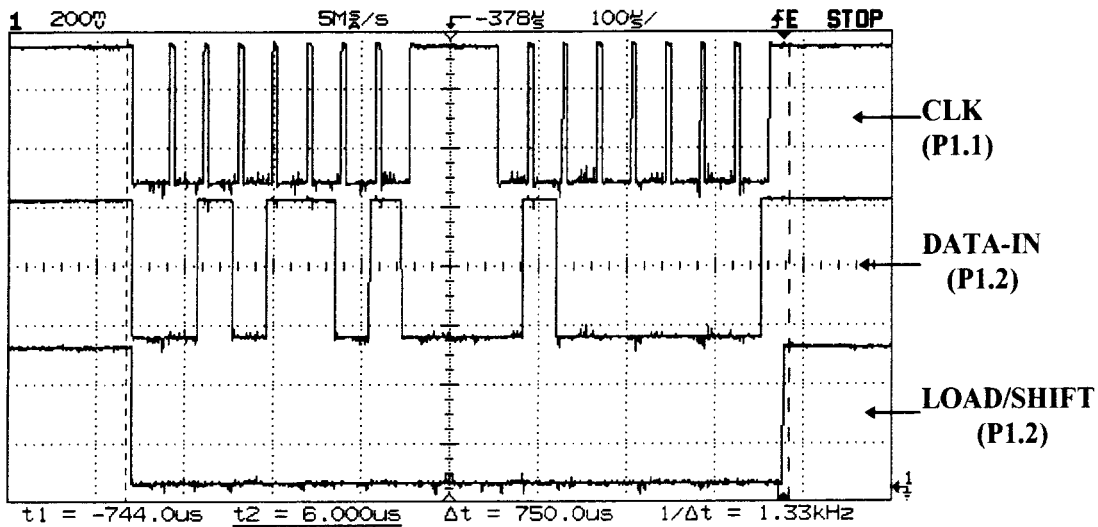


Figure 2



The rising edge of the clock signal appears approximately 32 μsec after the CC3 compare/capture unit in the μC generates an interrupt.