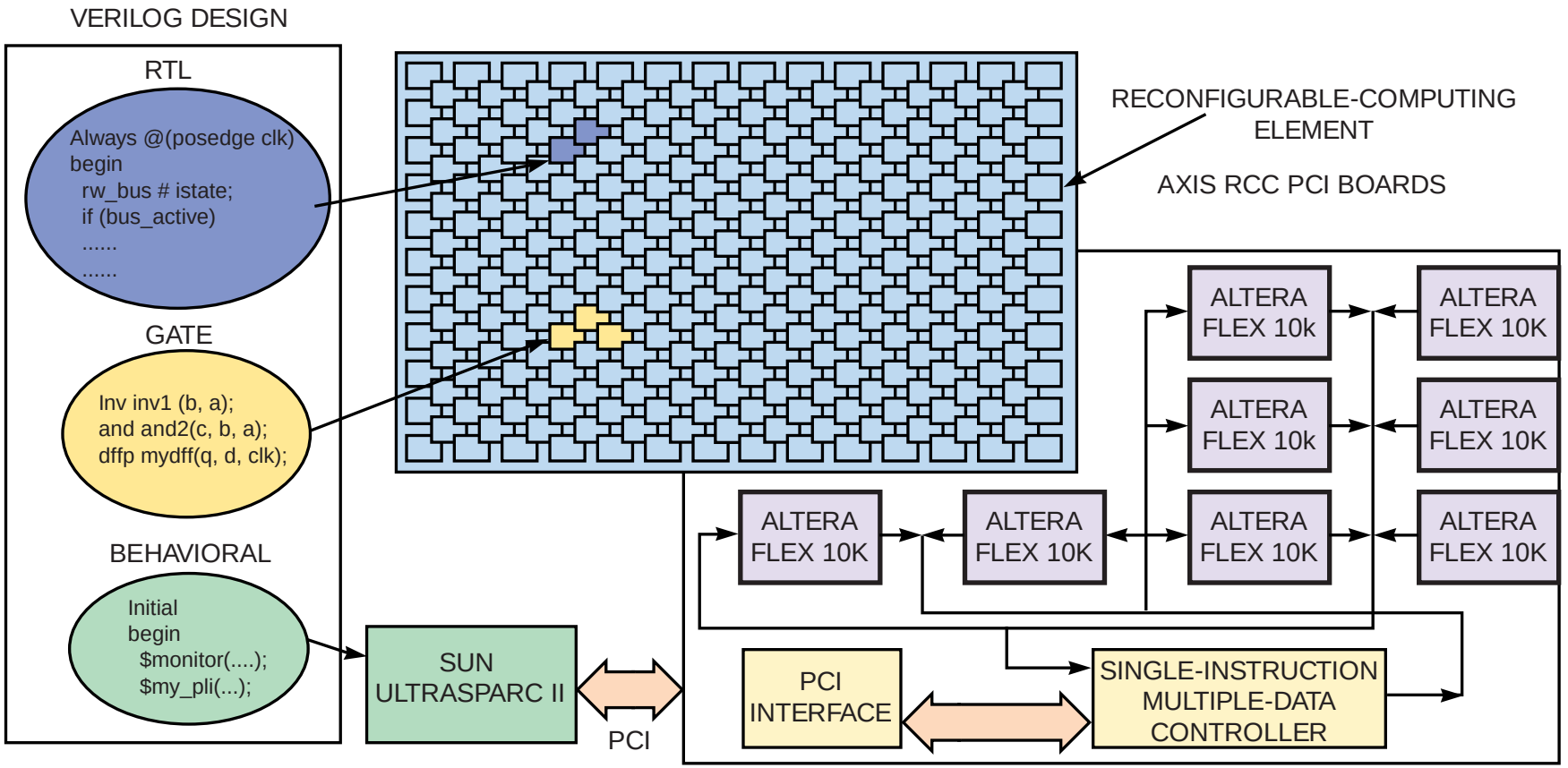

The Xcite-1000 partitions your design into various levels of design abstraction. The system maps gate-level and RTL portions of the design into Altera chips, which are reconfigured as design-specific computing elements. The hardware-based simulations run much faster than software-based gate-level or RTL simulations.