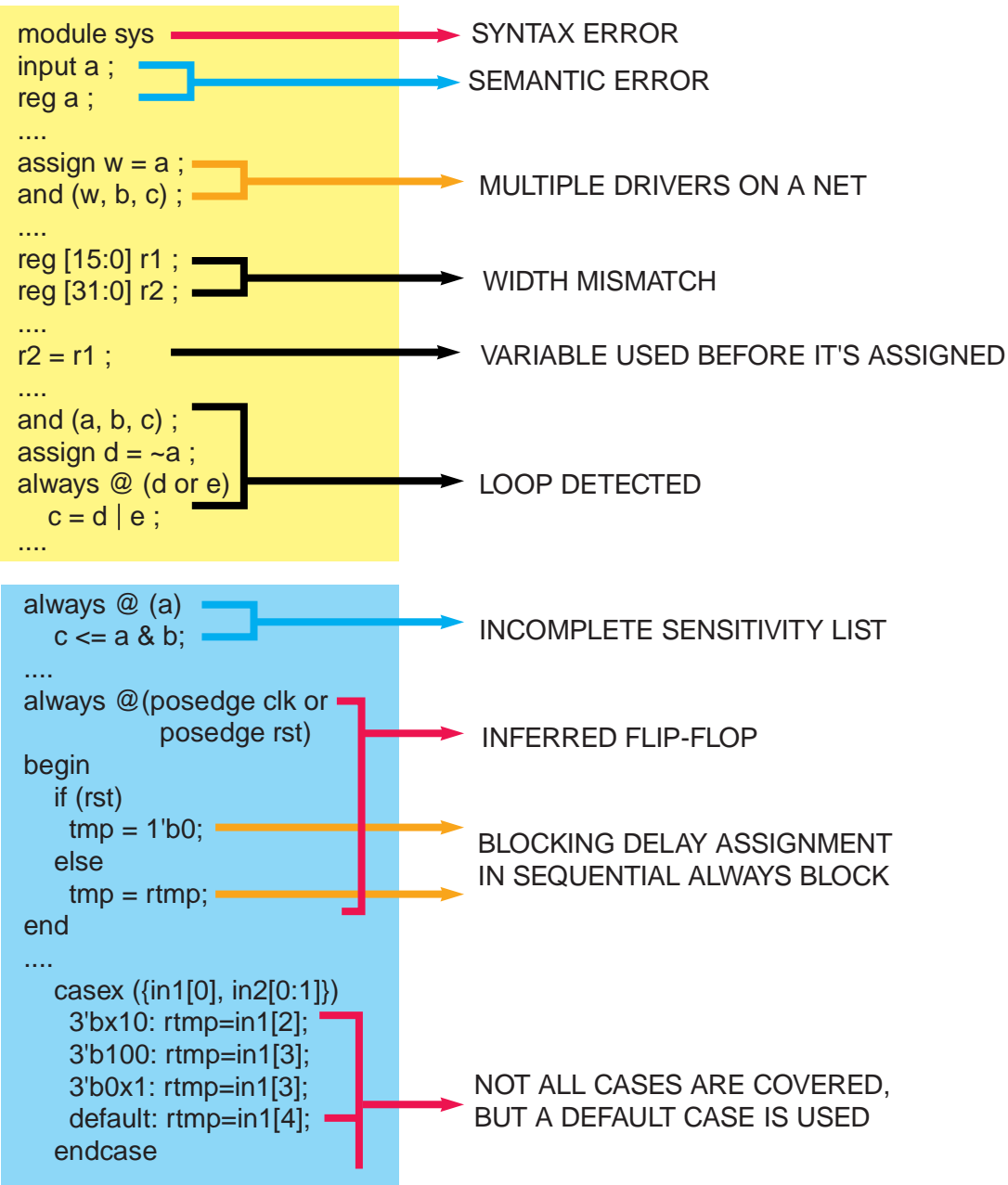


Figure 1



This RTL-code segment annotates examples of the kinds of Verilog-coding errors you can find by running a code-enhancement tool, such as Verilint, on RTL code.