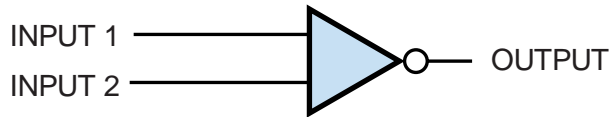


Figure A



INPUT 1	INPUT 2	OUTPUT
1	1	0
0	1	1
1	0	1
0	0	1

Fault-coverage testing of this NAND's output pin requires only input vectors (1,1) and (0,0). To verify that the simulator has completely exercised the NAND gate, a code-coverage tool checks whether the simulator has applied all four possible input vectors.