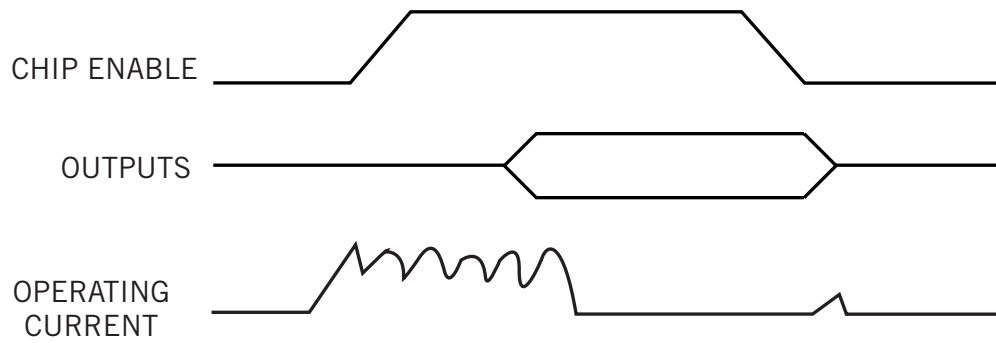
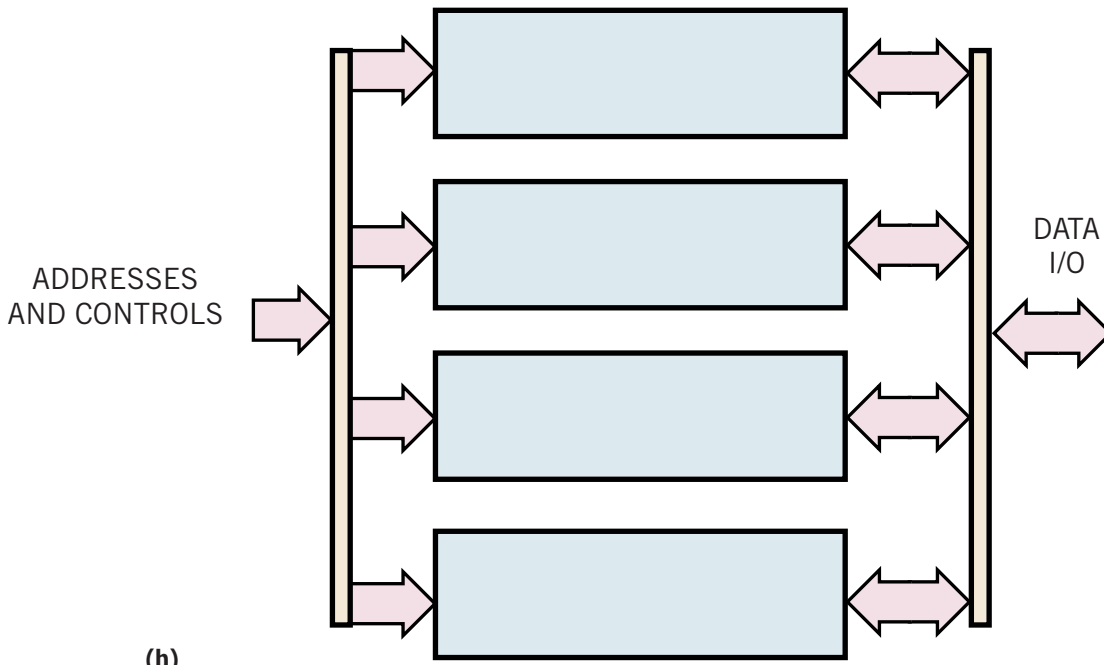


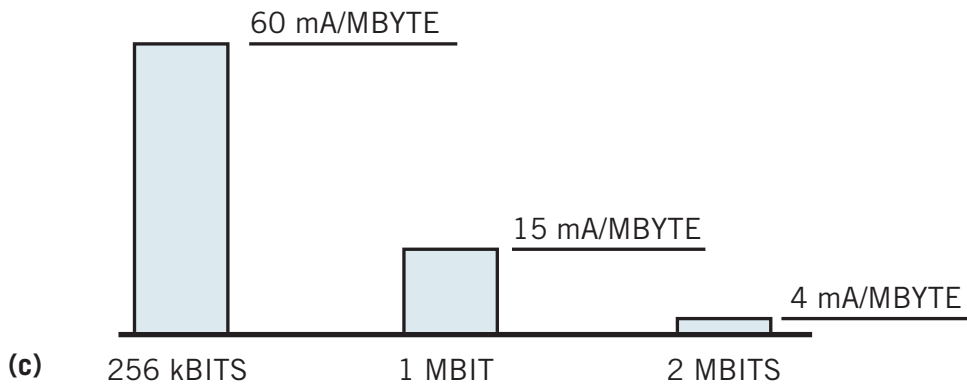
Figure 4



(a)



(b)



(c)

SRAM manufacturers commonly power down portions of the memory's internal circuitry (a) and subdivide the array (b) to reduce active and standby power consumption (c) (courtesy Enable Semiconductor).