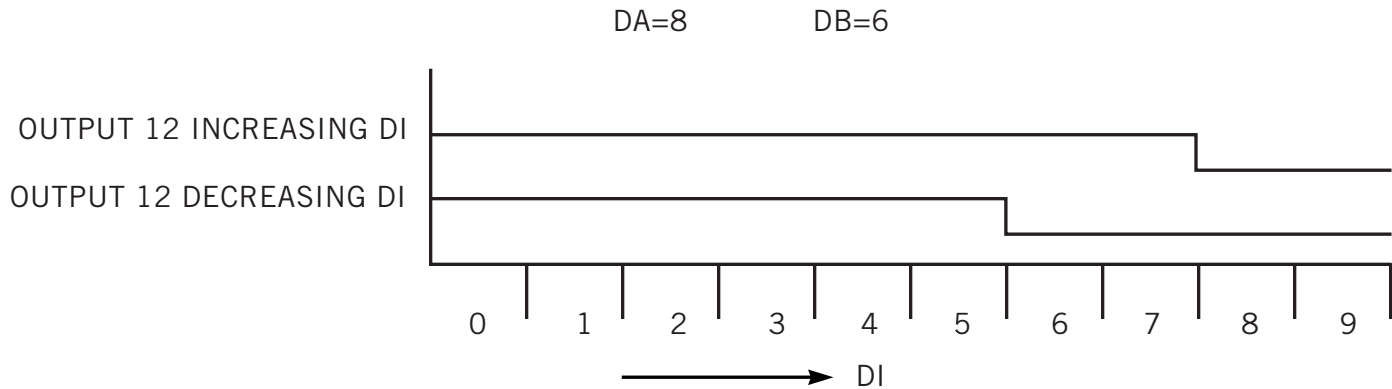


Figure 2



The hysteresis in Figure 1's circuit is the difference between the switching points for increasing and decreasing DI signals.