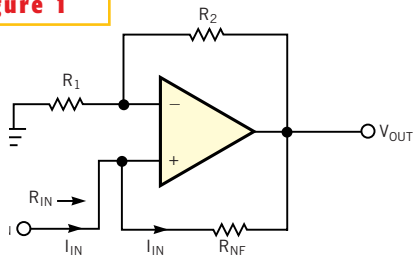


Figure 1



This circuit exhibits negative resistance at its input.