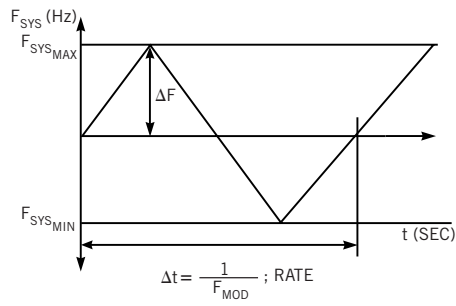


Figure 1



NOTES:

$$F_{SYS_MAX} = F_{SYS} + \{2\%, 4\%, 6\% \}.$$

$$F_{SYS_MIN} = F_{SYS} - \{2\%, 4\%, 6\% \}.$$

$$F_{MOD} = \frac{F_{REF}}{Q}, \text{ WHERE } Q = \{40, 80\}.$$

A modulation waveform illustrates frequency of the clock generated by FM-PLL over time.