

D ₇D ₀	A ₇A ₀
00000001	00000000
00000010	00000001
00000100	00000010
00001000	00000100
00010000	00001000
00100000	00010000
01000000	00100000
10000000	01000000
(a)	(b) 10000000

Figure 2

A walking-ones pattern is useful for testing a memory's data bus (a), whereas the similar, but not identical, power-of-two sequence exercises the address bus (b).