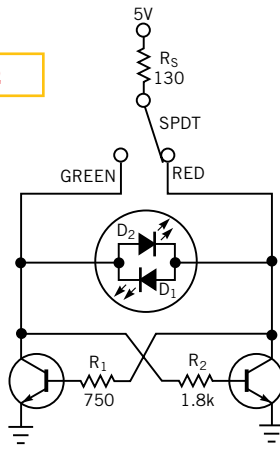


**Figure 2**



In this "flip-flop" switch, the only losses come from the  $V_{CE(SAT)}$  and the base currents of the transistors.