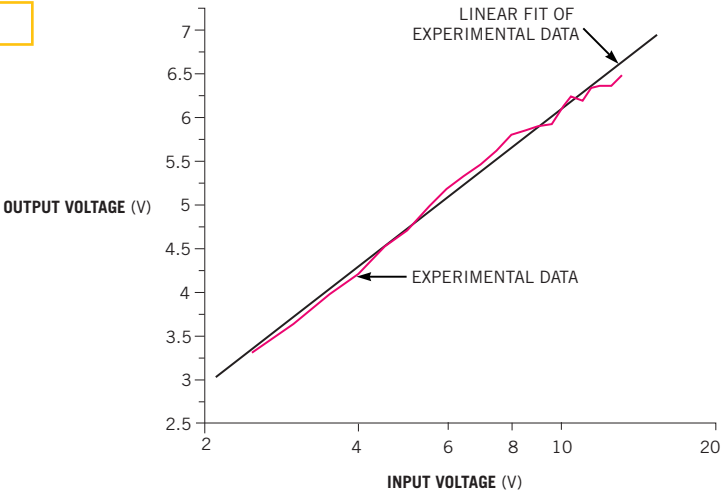


Figure 2



The circuit of Figure 1 produces a distinct logarithmic output.