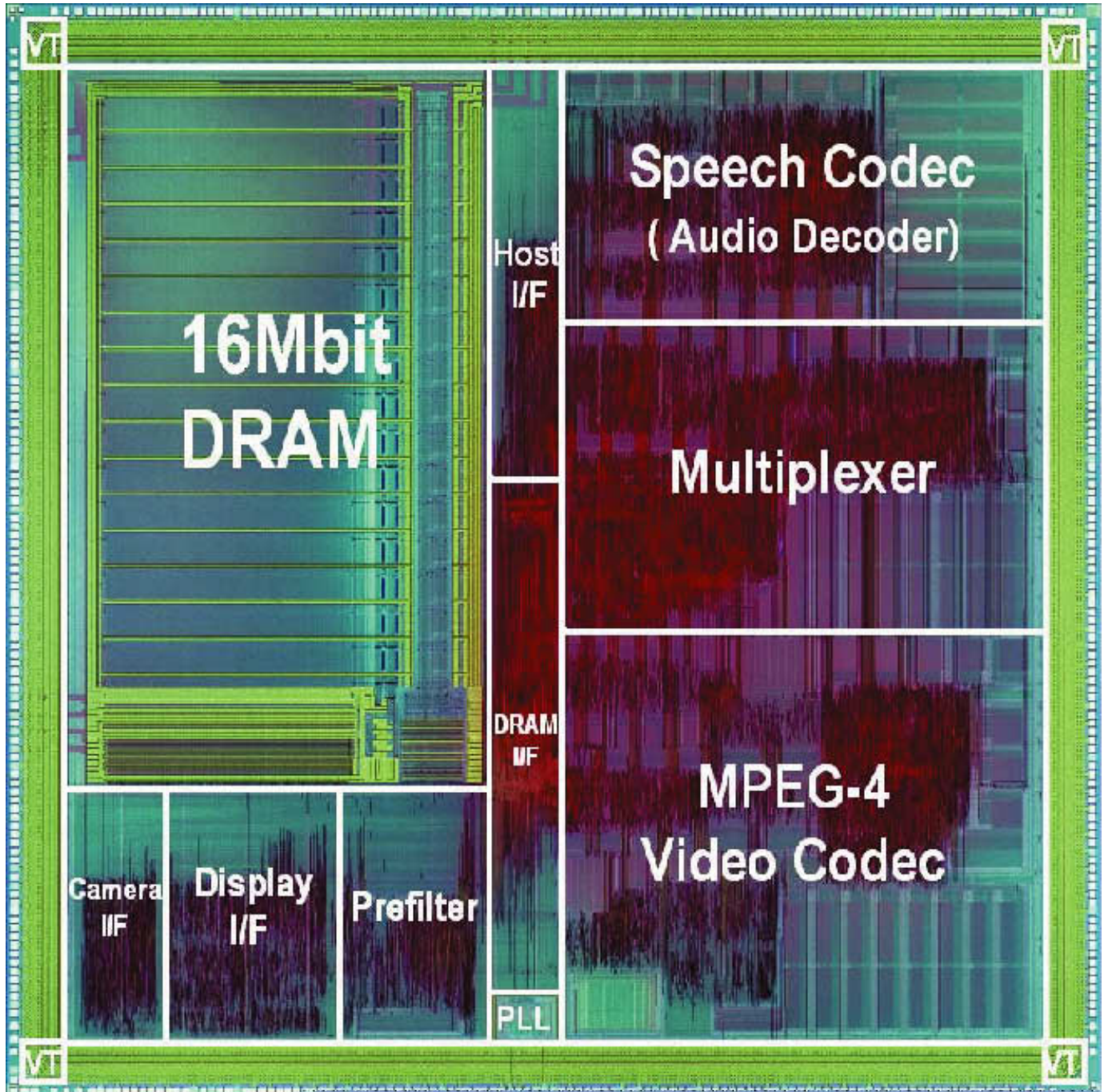


Figure 3



By embedding the memory and thereby reducing the amount of chip-to-chip interaction, you can further reduce power consumption at the system level (courtesy Toshiba).