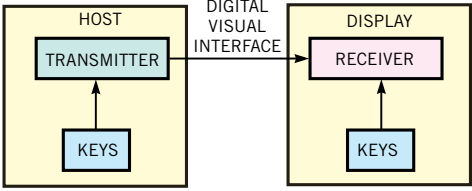


Figure 1



This block diagram represents an HDCP system in its simplest form.