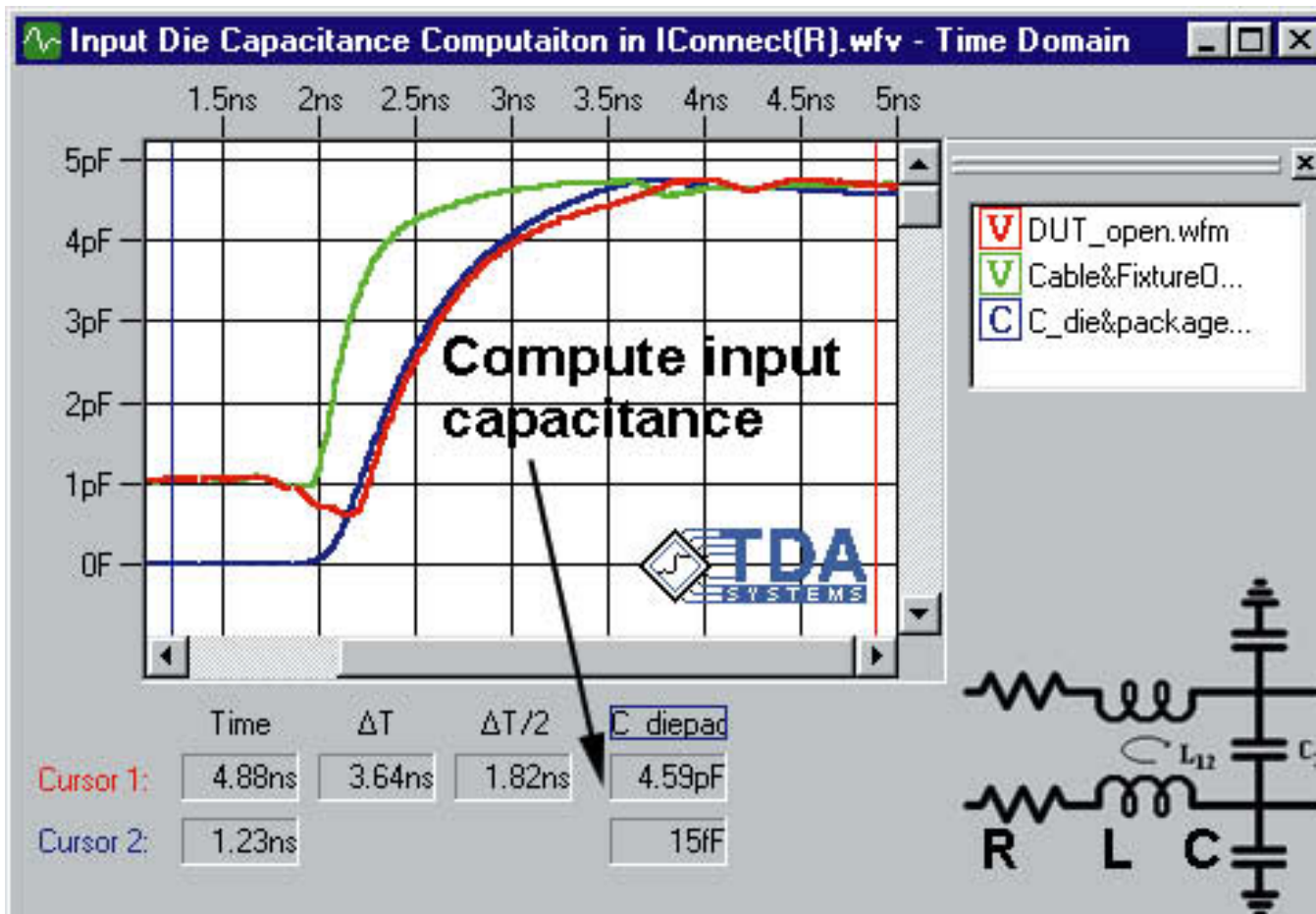


Figure 1



Input-package-and-die-capacitance computation is a two-part measurement process.