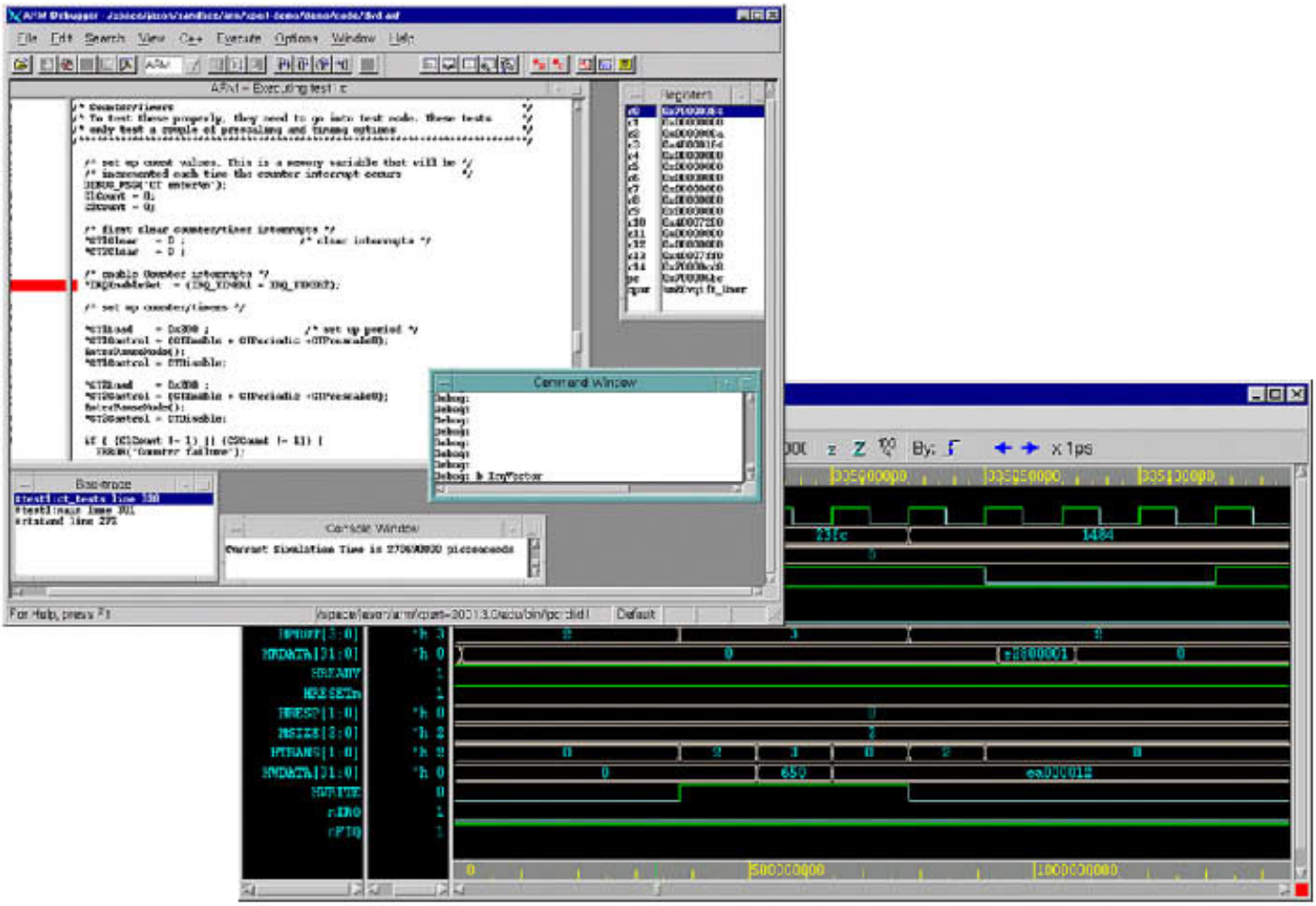
Figure 1



Efficient hardware/software co-verification employs standard development tools for debugging.