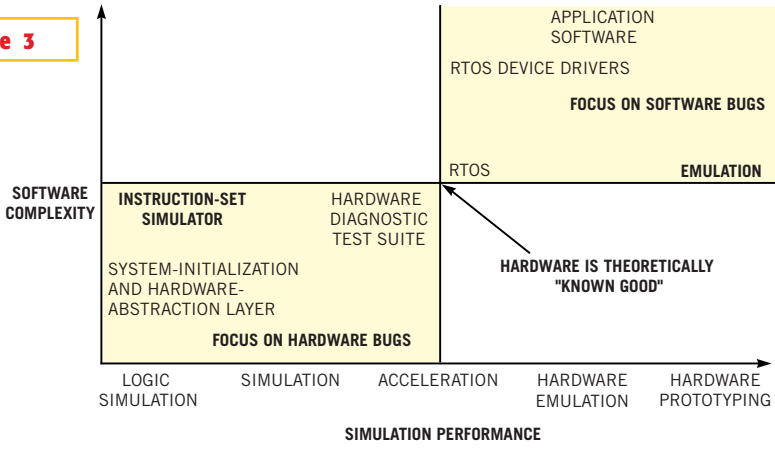


Figure 3



A complete methodology for debugging SOC designs concentrates on hardware during the early stages and then switches to software bugs.