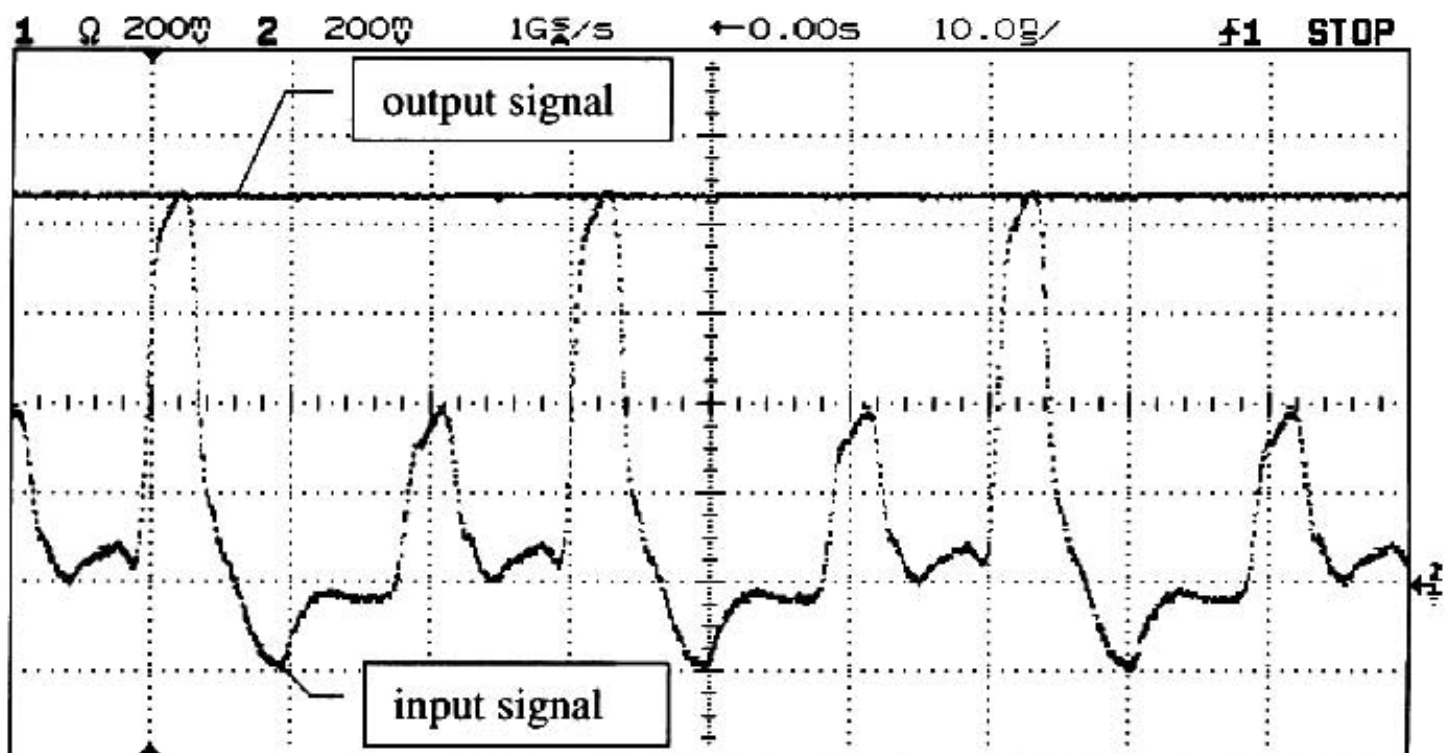


Figure 2



The circuit in Figure 1 yields a dc value equal to the positive peak of the input signal.