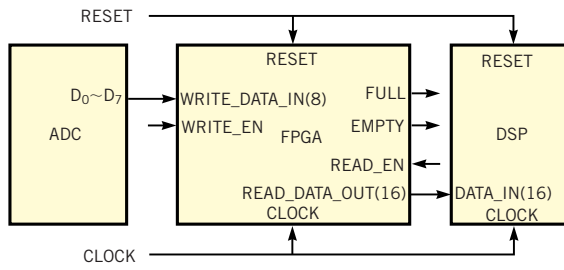


Figure 1



This block diagram is an example of a system using a FIFO between an ADC and a microprocessor chip.