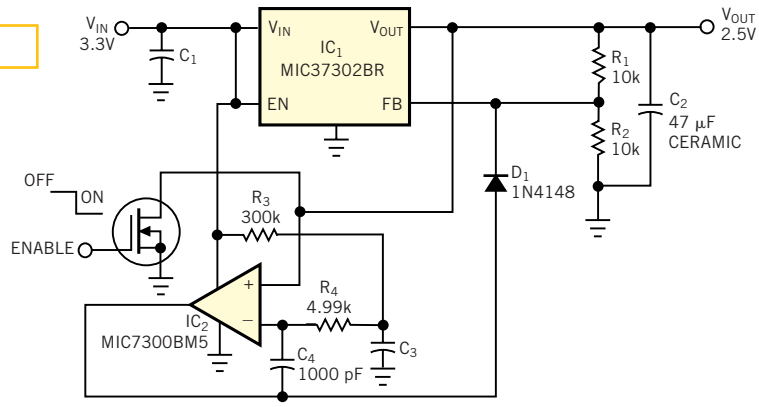


**Figure 1**



**This circuit controls the power-up sequencing for Xilinx's Spartan ICs.**