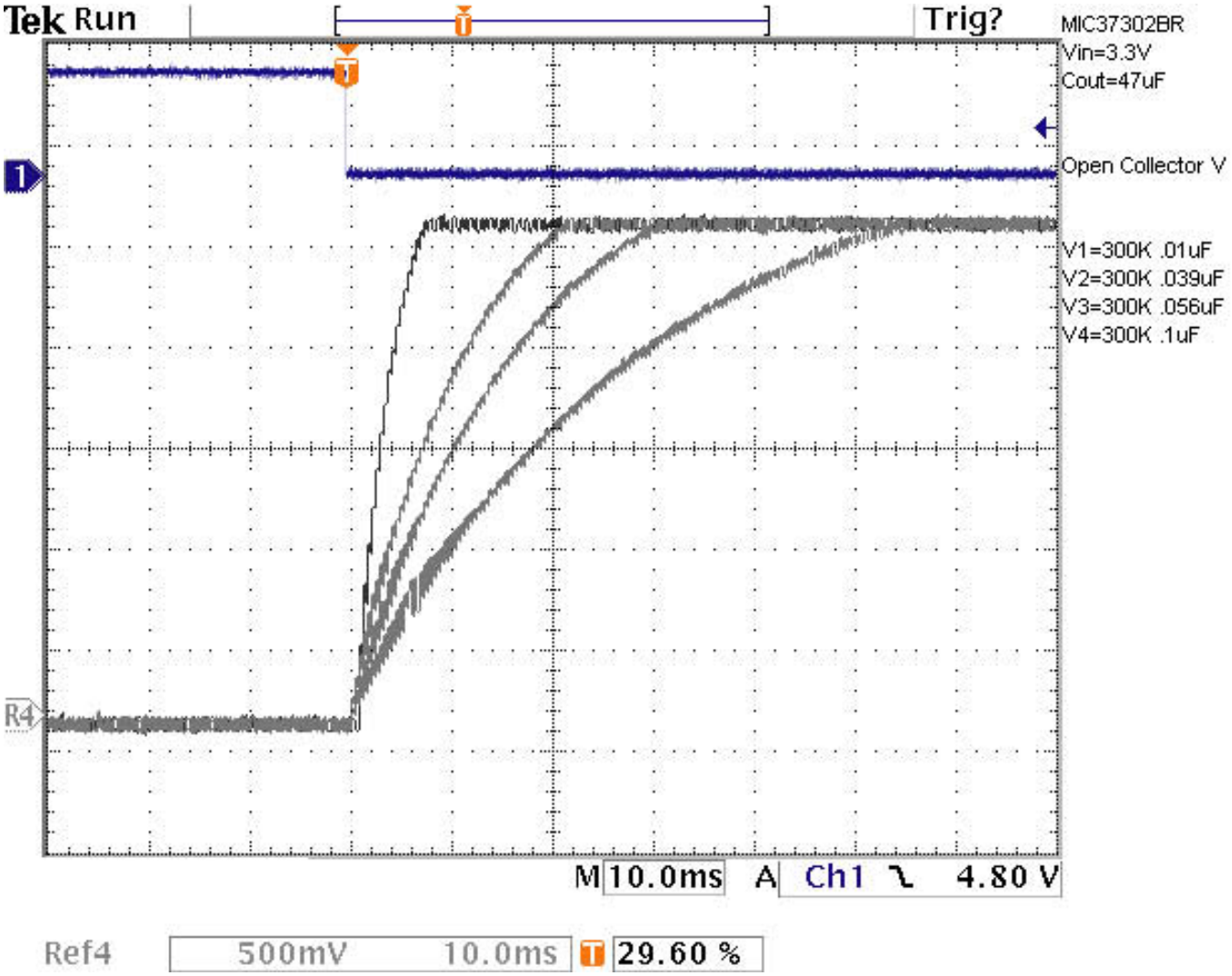
Figure 2



C_3 controls the rise time of the output of Figure 1's circuit.