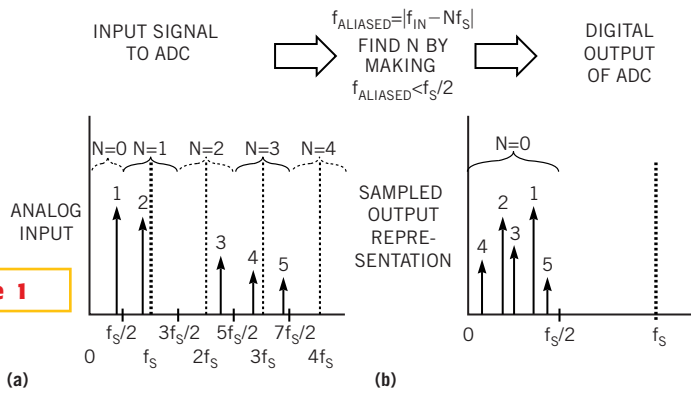


Figure 1



After conversion, the ADC preserves the magnitude of the input signal (a) but aliases the frequencies above half of the sampling frequency, f_s , back below $f_s/2$ (b).