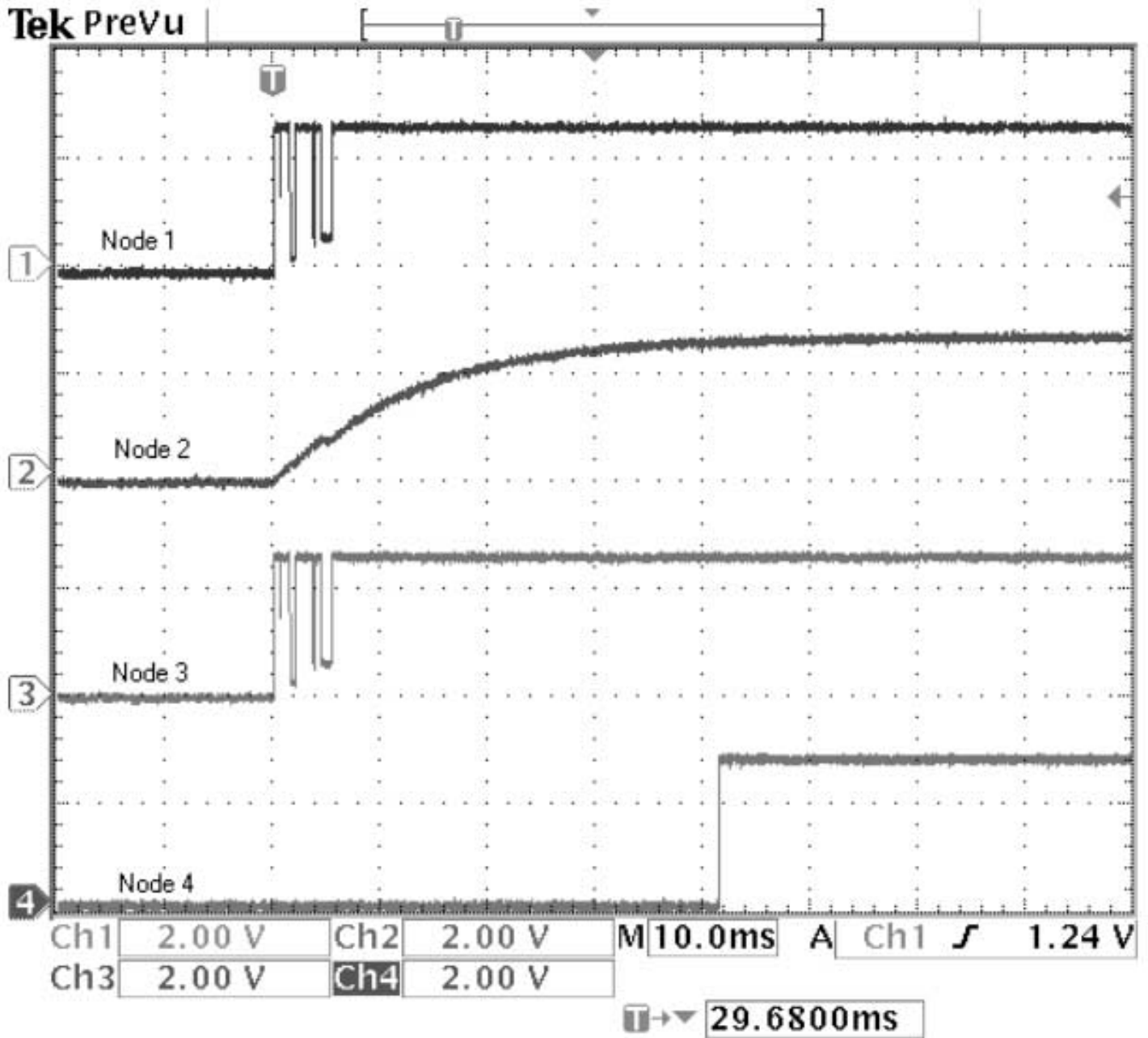


Figure 3



The circuit in Figure 2 enables the processor well after the stabilization of the power-supply voltage.