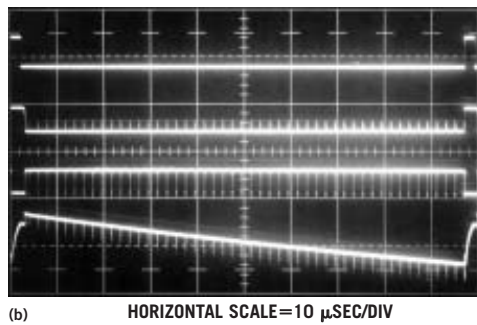


**Figure 3**

(a)

**A galvanically isolated thermistor digitizer has a 3500V breakdown barrier (a). IC<sub>1</sub> provides pulsed power (Trace A) to OSC<sub>1</sub> via Q<sub>1</sub>, Q<sub>2</sub> and T<sub>1</sub>. Q<sub>3</sub> extracts data and presents it to the output (Trace C) (b).**

- A (IC<sub>1</sub> OUTPUT, 5V/DIV)
- B (Q<sub>2</sub> COLLECTOR, 10V/DIV)
- C (DATA OUTPUT, 10V/DIV)
- D (IC<sub>1</sub> INPUT, 1V/DIV)



(b)