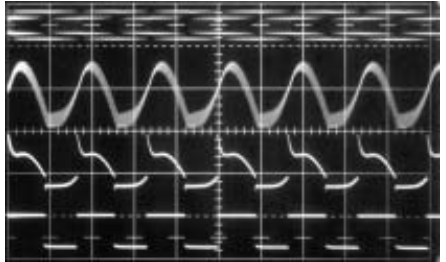


(a)

A (Q₁ BASE, 2V/DIV)
B (Q₁ COLLECTOR, 2V/DIV)
C (IC₂ + INPUT, 1V/DIV)
D (IC₂ OUTPUT, 5V/DIV)



(b) HORIZONTAL SCALE=50 μSEC/DIV

Figure 4 A counter-driven, sine-encoded memory produces a 0.75%-distortion sine wave via a DAC. The LTC1799 oscillator's frequency sets the output frequency.