

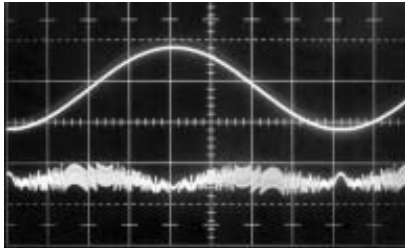
Clock-related products are evident in the distortion presentation of Figure 4's sine output (a). Fast oscillator-frequency shifting permits crisp changes in the output sine wave's frequency (b).

The sine wave's amplitude instantaneously and faithfully responds to the DAC-reference input step (c).

Figure 5

FIGURE 4'S
SINE OUTPUT
(2V/DIV)

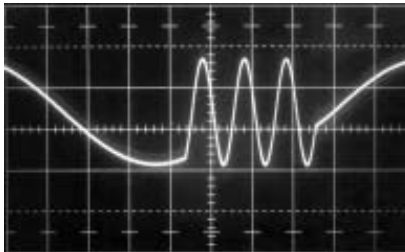
DISTORTION
(0.75%)



(a)

HORIZONTAL SCALE=2 mSEC/DIV

SINE OUTPUT
(1V/DIV,
UNCALIBRATED)

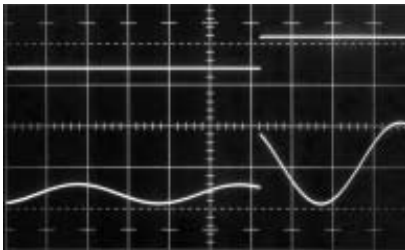


(b)

HORIZONTAL SCALE=2.5 mSEC/DIV

DAC-
REFERENCE
INPUT STEP
(2V/DIV)

SINE OUTPUT
(2V/DIV)



(c)

HORIZONTAL SCALE=5 mSEC/DIV