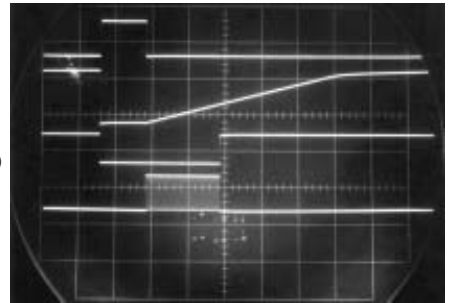


A (SYNCHRONIZED CONVERT COMMAND, 5V/DIV)

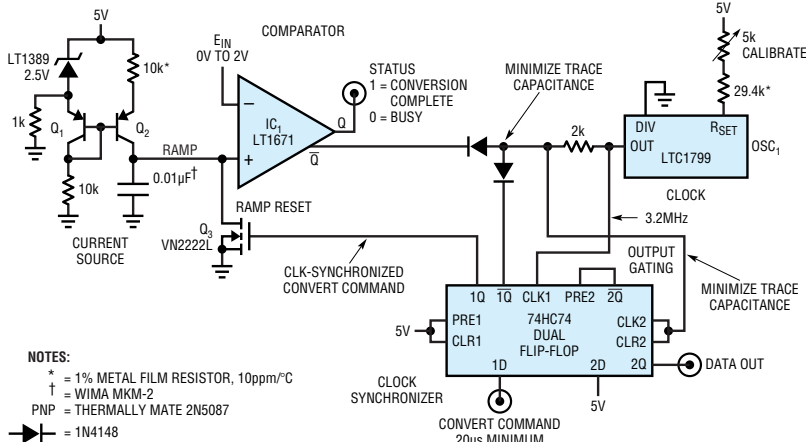
B (RAMP, 2V/DIV)

C (STATUS, 5V/DIV)

D (DATA OUT, 5V/DIV)



(b) HORIZONTAL SCALE=20 μSEC/DIV



NOTES:

* = 1% METAL FILM RESISTOR, 10ppm/°C

† = WIMA MKM-2

PNP = THERMALLY MATE 2N5087

→ = 1N4148

(a)

Figure 8 A simple 8-bit ADC has a passive, high-impedance input and an 80-μsec conversion time (a). A synchronized convert command (b, Trace A) begins a reference ramp and forces the circuit status output low (Trace C). When the ramp crosses E_{IN} 's voltage, the circuit output's clock burst ceases (Trace D).