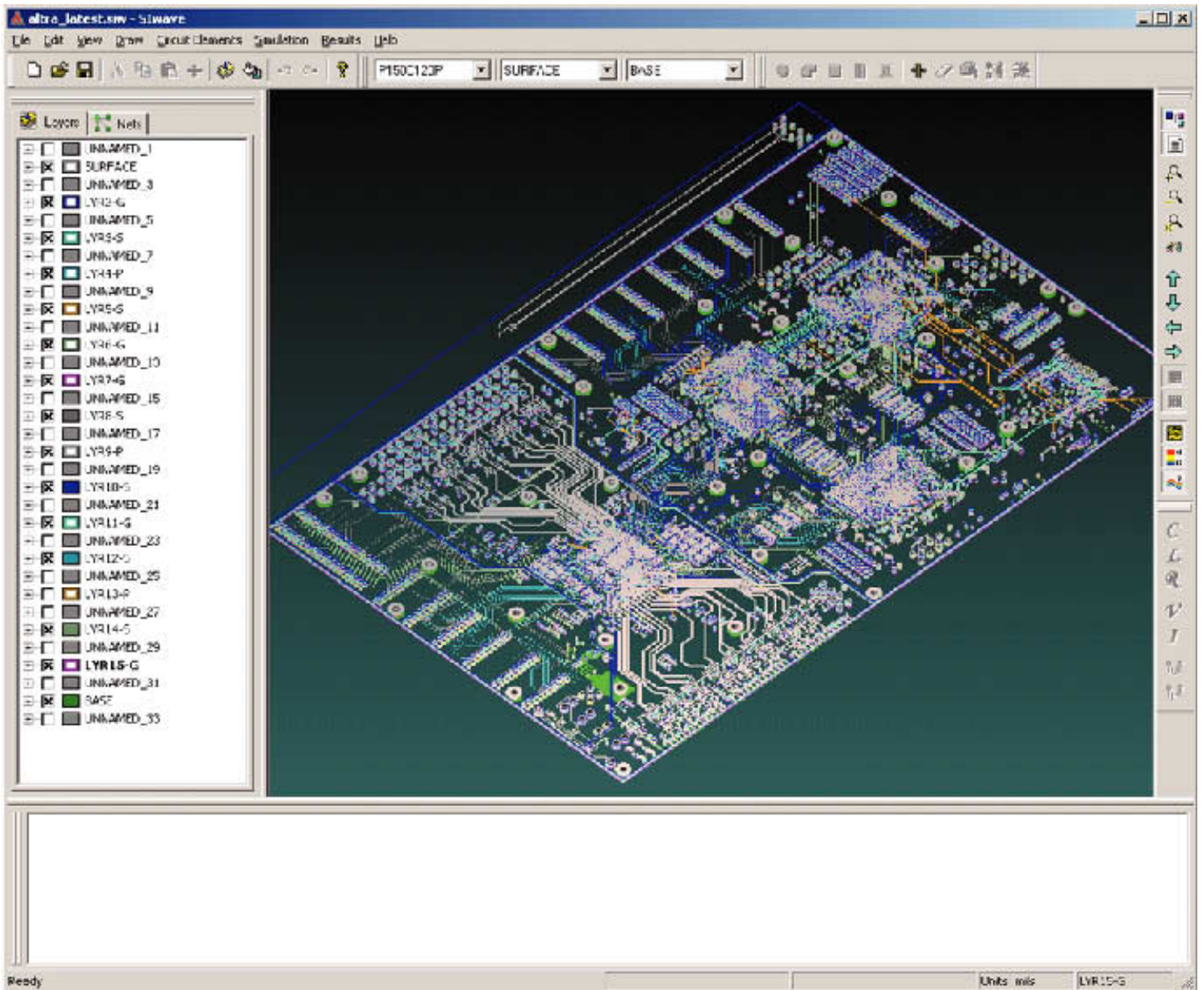


Figure 1



The xDSM board in Siwave contains two high-speed buses on the left side of the board and three Xilinx FPGA packages on the right side.