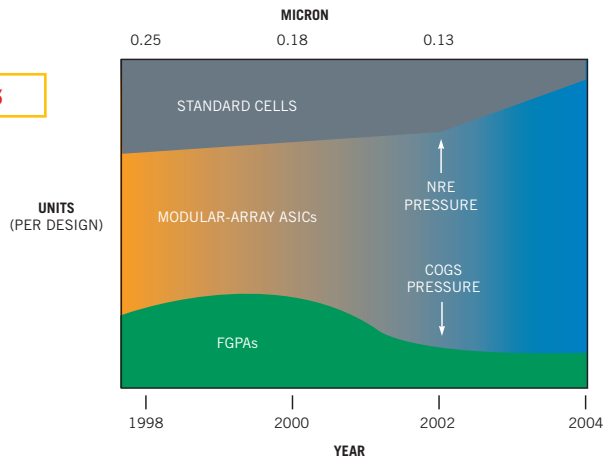


Figure 3



Structured-ASIC vendors' presentations all have graphs that show their products filling the gap between standard-cell ASICs and FPGAs. Competitors claim the chips are nothing more than doomed-to-fail attempts at reviving the near-dead gate array (courtesy Lightspeed Semiconductor).