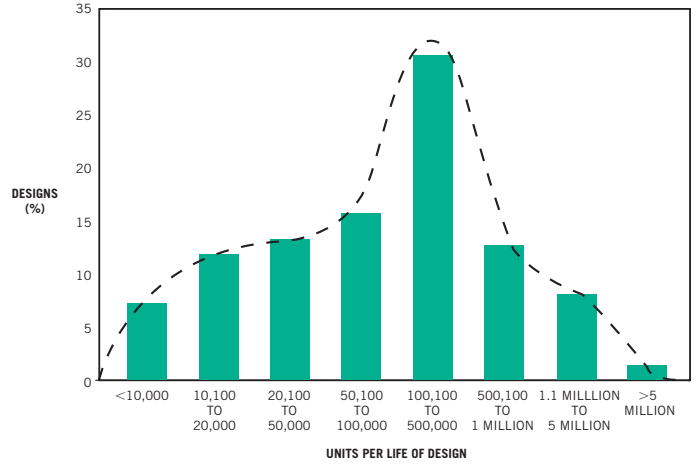


(a)



(b)

Figure 6

Designs with low to moderate numbers of transistors and, therefore, gates, encompass most of the ASIC market (a), and most customers also use ASICs in low to moderate volumes (b) (courtesy Xilinx and Leopard Logic, respectively).