

TABLE 1—THE PHASE-REGISTER CONTENTS YIELD FOUR OUTPUT LEVELS FROM THE CIRCUIT IN FIGURE 1

	Device number	Phase-register values (°)		Normalized level of summed signal	Output level with $R_{\text{TERM}} = 100\Omega$ and $R_{\text{SET}} = 6.8\text{ k}\Omega$ (mV)
P ₀	A	45	$P_{0A} + P_{0B}(45 + 180)^\circ$	0.765	223
P ₁	A	95	$P_{0A} + P_{1B}(45 + 210)^\circ$	0.426	128
P ₀	B	180	$P_{1A} + P_{0B}(95 + 180)^\circ$	1.47	435
P ₁	B	210	$P_{1A} + P_{1B}(95 + 210)^\circ$	1.191	350