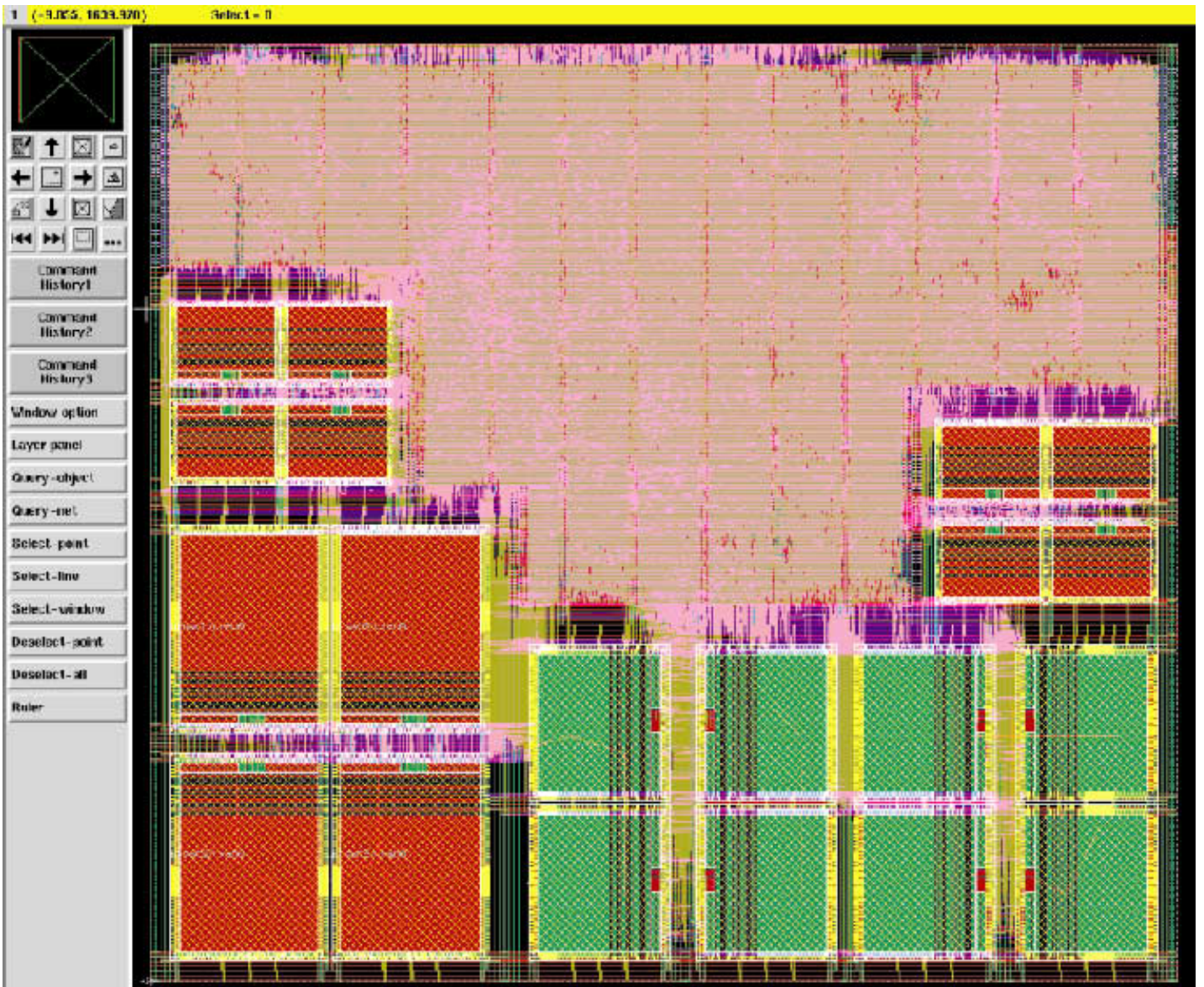


Figure 3



Hardening a core, such as the ARM946E-S, demands utmost versatility from the design-planning process. You need to implement features such as power and signal pins so that system-on-chip designers can use the core in a variety of contexts.