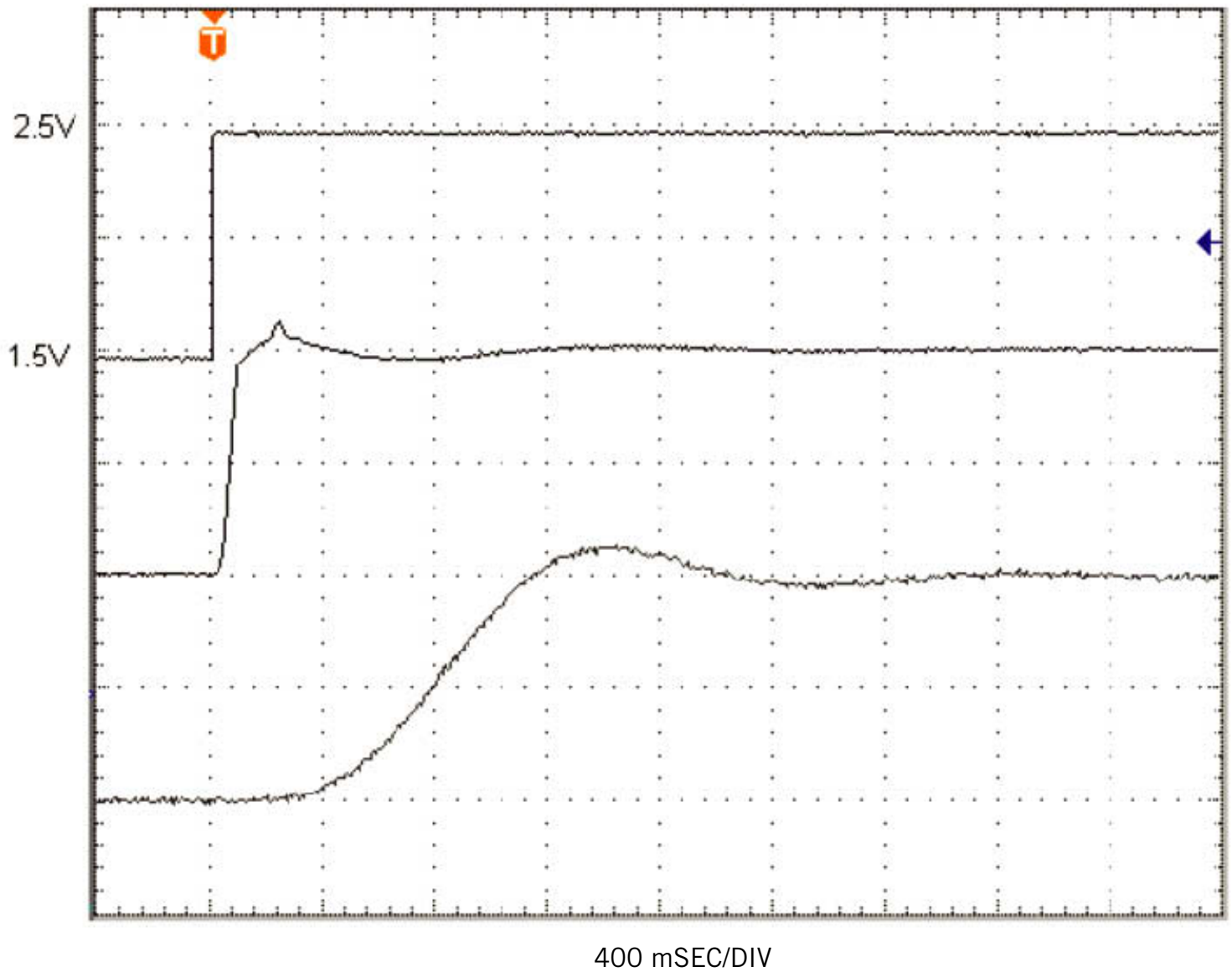


Figure 2



**These traces show the time-domain response for the circuit in Figure 1 with optimization circuitry (middle trace) and without it (bottom trace).**