

TABLE 1—MAX II FEATURES, PRICES, AND PRODUCTION TIMES

Device	Logic elements	Equivalent macrocells¹	User I/O pins	Packages²	Price (500,000)	Production availability
EPM240	240	192	80	T100	\$1.50	Fourth quarter 2004
EPM570	570	440	160	T100, T144, F256	\$2.30	Fourth quarter 2004
EPM1270	1270	980	212	T144, F256	\$4.25	Third quarter 2004
EPM2210	2210	1700	272	F256, F324	\$7	First quarter 2005

¹ Average across more than 300 reference designs.

² F=1-mm FineLine BGA; T=0.5-mm TQFP.