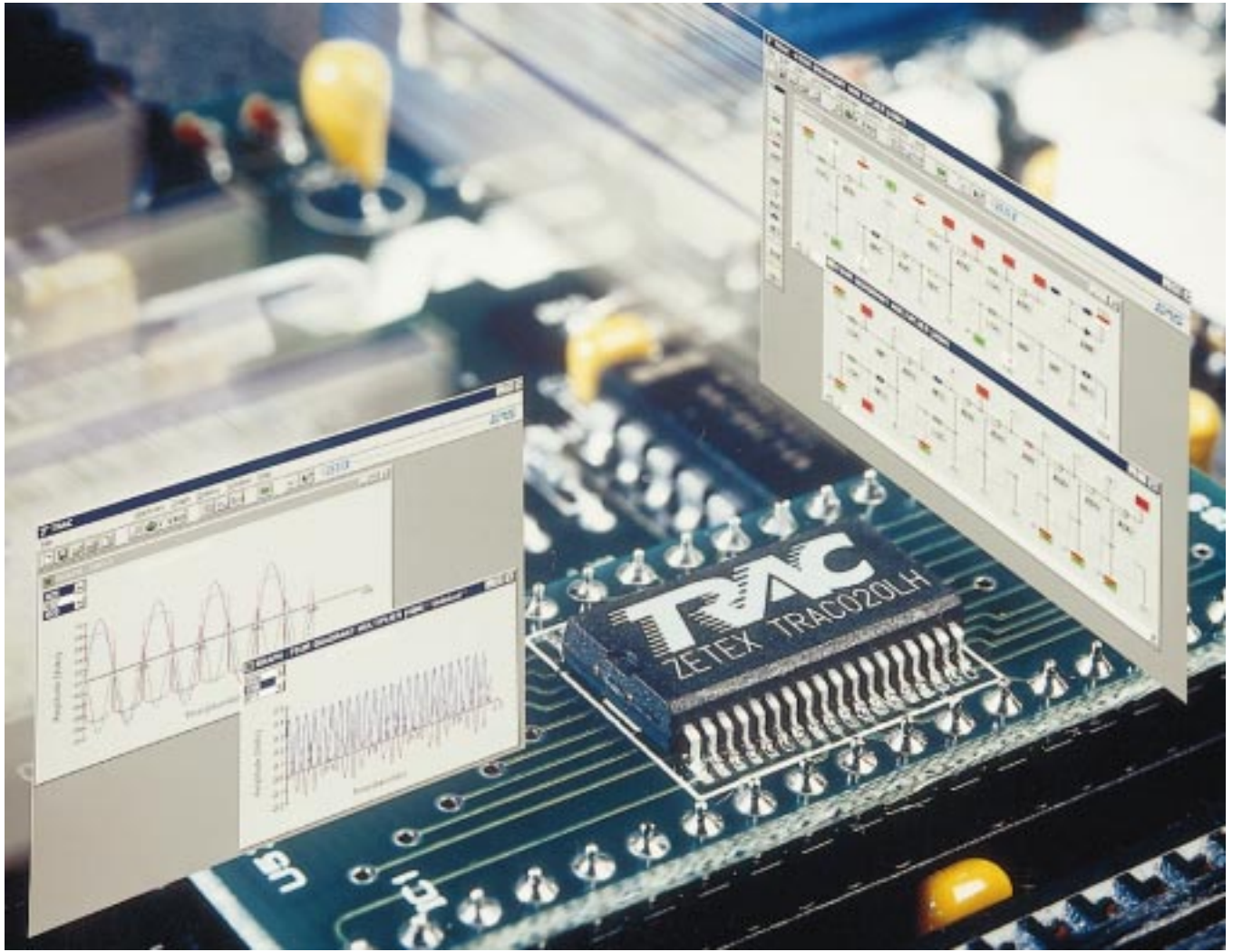


**Figure 7**



**The TRAC family from Zetex lets you assemble and define the elementary functions of its analog blocks, perform simulation, and then load the configuration into internal memory.**