



Figure 4

If the demodulator's reference signal is in phase with the times-one clock, the demodulator output resembles a distorted sine wave in which the positive "half-cycle" duration assumes one of three values. The shortest duration represents a change from 1 (in the previous clock period) to 0 (in the current period). The longest duration represents a change from 0 to 1. The intermediate value indicates that the data in the current clock period has not changed from that in the previous period.