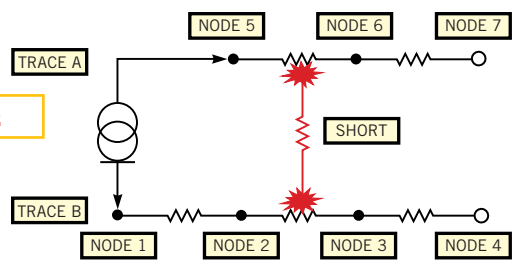


**Figure 2**



The equivalent circuit of the pc-board layout shows the principal of the source-and-probe technique.