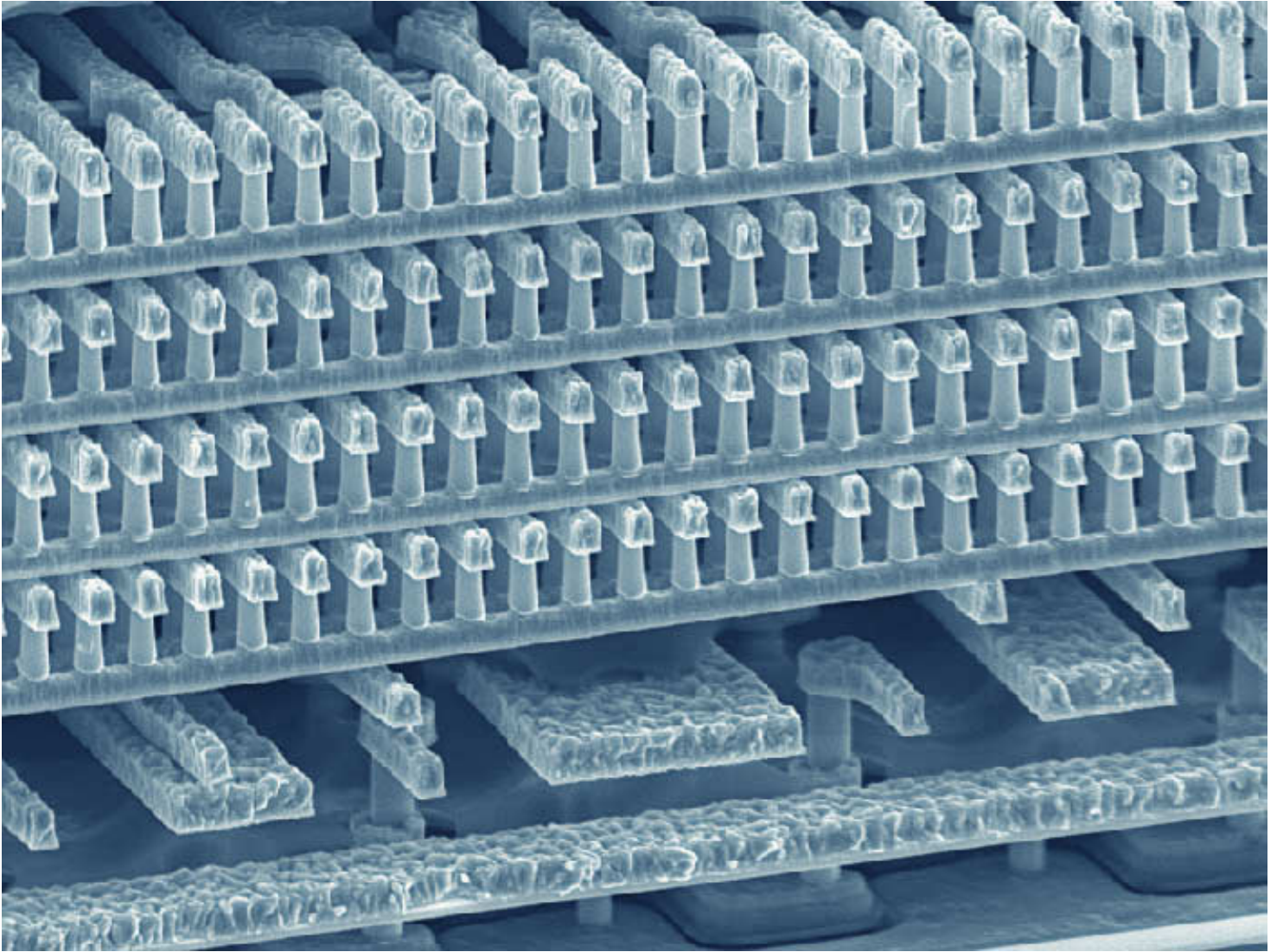


Figure 5



Three-dimensional memories place the periphery circuitry below the storage arrays; conventional approaches place them alongside each other (courtesy Matrix Semiconductor).