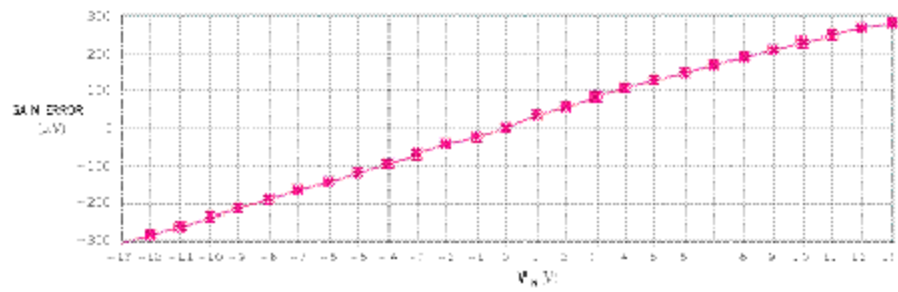


Figure 3



The circuit in Figure 2 introduces a full-scale gain error of less than 300  $\mu\text{V}$  over a 26V input-voltage range.