

**TABLE A—EXAMPLE RATIOS BETWEEN INCOMING DATA-SAMPLE FREQUENCY ( $f_s$ ) AND SYSTEM CLOCKS**

Sampling frequency (kHz)	System ratio ( $xf_s$ )	System clock (MHz)	Multiplier	Interface circuit ratio	Modulator master clock (MHz)
48	256	12.3	$\times 3$	None	36.9
	384	18.5	$\times 2$	None	36.9
96	56	24.6	$\times 3$	$\div 2$	36.9
	384	36.9	$\times 2$	$\div 2$	36.9
192	256	49.2	$\times 3$	$\div 4$	36.9
	384	73.7	$\times 2$	$\div 4$	36.9