



NOTE: AC-COUPLED ON 3.3V DC.

Figure 6 A switching regulator outputs simulator waveforms, in which the function generator supplies ripple-path (Trace A) and spike-path (Trace B) information. C_1 and C_2 compare the differentiated spike information's bipolar excursion (Trace C), resulting in Traces D and E's synchronized spikes. Diode-gating inverters present Trace F to spike-amplitude control. G_1 sums spikes with dc-ripple path from power amplifier IC_1 , forming linear-regulator input (Trace G). (Spike width is abnormally wide for photographic clarity.)
